

Amendments

In the Claims, kindly replace all prior versions, and listings, of claims in the application with the following:

1. (Currently amended) A computer device with a safety function for avoiding non-necessary disconnection of the computer device, comprising:

processor means (10);

an ordinary memory unit (12) connected to said processor means (10) and arranged to ~~comprise~~ store at least one application program that is executed by the processor means (10);

a supervisory unit (14) that supervises the function of the computer device and that is arranged to, in case an error occurs, send a restart signal or a stop signal to the processor means (10),

characterized by

a further memory unit (16) that is arranged to ~~comprise~~ store at least some basic ~~system~~ instructions in the form of instructions of the at least one application program that control a system or a part of a system that is controlled by the computer device, wherein the computer device is arranged such that the processor means (10), at a restart generated by said restart signal from the supervisory unit (14), is connected to the further memory unit (16) and reads and executes the basic instructions that are stored in the same, while the ordinary memory unit (12) is disconnected from the processor means (10).

2. (Original) A computer device according to claim 1, wherein the ordinary memory unit (12)

and the further memory unit (16) constitute two different, physically separate memories.

3. (Original) A computer device according to claim 1, wherein the ordinary memory unit (12) and the further memory unit (16) constitute two parts of physically the same memory, but with different memory addresses.

4. (Previously presented) A computer device according to claim 1, where in said supervisory unit (14) is arranged to generate a signal in dependence of a timer (18) in such a manner that said restart signal is generated if no trigger-signal signal that sets the timer (18) to zero is received within a predetermined time interval.

5. (Previously presented) A computer device according to claim 1, comprising a memory safety circuit (20) that is arranged to stop the reading from the ordinary memory unit (12) and to connect for reading from said further memory unit (16) when both said restart signal and a signal indicating applied supply voltage is the case.

6. (Currently amended) A computer device according to claim 1, wherein said further memory unit (16) is arranged such that it ~~comprises~~ stores the basic ~~system~~ instructions with a high degree of reliability.

7. (Currently amended) A computer device according to claim 1, wherein said further memory unit (16) is arranged such that it ~~comprises~~ stores the basic ~~system~~ instructions with a high degree of reliability that is higher than the degree of reliability that is the case in the ordinary

memory unit (12).

8. (Previously presented) A computer device according to claim 1, wherein at least said further memory unit (16) is a non-volatile memory.

9. (Previously presented) A computer device according to claim 1, wherein said processor means (10) comprises a working memory (22) that is arranged such that at a restart of the computer device this working memory (22) is reset before reading from said further memory unit (16) is started.

10. (Previously presented) A computer device according to claim 1, arranged such that if said restart signal has been generated a predetermined number of times, then, in case an error occurs again, said stop signal is generated.

11. (Previously presented) A computer device according to claim 1, comprising a switching member (24) for manually generating said restart signal.

12. (Currently amended) A computer device arranged to secure the normal function of the computer device, under the execution of at least one application program, also when an error occurs that normally leads to disconnection and shut-off of the computer device or at least to disconnection concerning said application program, which computer device comprises processor means (10), an ordinary memory unit (12) connected to said processor means (10) and arranged to compromise at least an said application program that is executed by the processor

means (10), a supervisory unit (14) that supervises the function of the computer device and that is arranged to, in case an error occurs in the execution of at least said application program, send a restart signal or a stop signal to the processor means (10),

characterized by

a further memory unit (16) that is arranged to comprise at least some basic ~~application~~ instructions for said application program, wherein the computer device is arranged such that ~~always~~ when a restart takes place in response to a restart signal generated by the supervisory unit (14), the processor means (10) is connected to the further memory unit (16) and reads and executes the basic instructions ~~that are stored in the same~~, while the ordinary memory unit (12) is disconnected from the processor means (10), wherein the computer device is arranged such that the execution of ~~the an~~ application that is controlled by said application program may continue on the basis of the ~~application~~ basic instructions that are ~~retrieved~~ read and executed from the further memory unit, wherein the execution of the application ~~in question~~ may continue without the necessity for the computer device to be disconnected, and wherein said further memory unit (16) is arranged to be write protected at least when the computer device is in operation.

13. (Currently amended) A method for securing the normal function of a computer device, under the execution of at least one application program which controls an application, ~~also~~ when an error occurs that normally leads to disconnection and shut-off of the computer device or at least to disconnection concerning said application program, which computer device comprises processor means (10),
an ordinary memory unit (12) connected to said processor means (10) and arranged to comprise at least ~~one~~ said application program that is executed by the processor means (10),

a supervisory unit (14) that supervises the function of the computer device and that is arranged to, in case an error occurs in the execution of at least said application program, send a restart signal or a stop signal to the processor means (10),

a further memory unit (16) that is arranged to comprise at least some basic ~~application~~ instructions for said application, wherein said further memory unit (16) is arranged to be write protected at least when the computer device is in operation,

which method comprises that ~~always~~ when a restart takes place in response to a restart signal generated by the supervisory unit (14), the processor means (10) is connected to the further memory unit (16) and reads and executes the basic instructions ~~that are stored in the same~~, while the ordinary memory unit (12) is disconnected from the processor means (10), wherein the execution of the application that is controlled by said application program may continue on the basis of the ~~application~~ basic instructions that are ~~retrieved~~ read and executed from the further memory unit such that the execution of the application in question may continue with out the necessity for the computer device to be disconnected.

14. (Currently amended) ~~Use of a computer device~~ A method according to any of the claims + 13, for controlling a system that is included in an aircraft.